Serial No.: 10/668,060 -2- Art Unit: Unassigned

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In the Specification

Applicants present amended paragraphs below indicating the changes with insertions indicated by underlining and deletions indicated by strikeouts and/or double bracketing.

Please amend the abstract as follows:

A method of fast random access management of a DRAM-type memory is provided.[[,]] including The method includes the steps of[[:]] dividing the memory into memory banks accessible independently in read and write mode[[;]], identifying the address of the bank concerned by a current request[[;]], and comparing the address of the bank concerned by a current request with the addresses of the N-1 banks previously required, N being an integral number of cycles necessary to the executing of a request[[;]], and if If the address of the bank concerned by a current request is equal to the address of a bank corresponding to one of the N-1 previous requests, the method includes suspending and memorizing storing the current request until the previous request involving the same bank is executed, otherwise, executing it.

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In the Drawings

Applicants have amended Figure 1 to include the legend "Prior Art." The Examiner is respectfully requested to review and enter the amendments to Figure 1.

A request for approval of proposed drawing corrections is attached.